START INH [1]

STD = Q1 = 1
100HZ = Q2 = 1
DBL_PLS = Q4 = 1
PIV = Q8 = 1

U1 4028
+15V
C1 .1
STD [3] SW1-2
100HZ [3] SW1-3
DBL-PLS [3] SW1-1
PIV [3] SW1-4
A VDD Q0 Q1 Q2 Q3 Q4 Q5 Q6 Q7 Q8 Q9 VSS D

U25 LM339
U2 LM339
C2 .1
C47 .1

SELECT ERROR
DS1 RED
+5V
R184 270
Q10 VN10KM
R8 49.9K
U28 4049
R116 270
1 = CONFIG ERROR
SEL >1 =1

U31 4071
U29 4049
SCA_INH [3]

1 = AMP START INHIBIT

R9 20K
R10 20K

R112 270 SEL ERROR J21-48

AMP_STRT [4]

U28 4049
R113 270 STD SW1-2 J21-45
R114 270 100HZ SW1-3 J21-46
R115 270 DBL_PLS SW1-1 J21-47
U29
R117 270 PIV SW1-4 J21-37

A SIM NOK [5]

C96 .1
R83 20K
SIM SENSE + J20-7
J20-8
R82 1.00K
C98 .1
U50 LM324

C97 100pf
R241 10K
R242 100
AMP SIM LOGIC J21-3 J24-8

R243 20K
AMP DISABLE N.O. J24-7

U28 4049
R505 20K
Q7 VN10KM
R501 100
AMP SIM LED J21-4

AMP LAMP J24-6
R291 56.1W
R292 20K
AMP DISABLE N.C. J24-4
J24-3
R293 20K
Q24 VN10KM
R294 270
AMP DISABLE LAMP J24-5

LAMP ON WHEN AMP DISABLED

SIZE	FSCM NO.	DWG. NO.	REV.
D		0449-5030	A
SCALE		11-04-94	SHEET 2 OF 7